



Housed Varistor

ThermoFuse Varistor

Series/Type: T14K130...420E2
Ordering code: B72214T2***K105
Date: 2013-01-019
Version: c

Applications

- ◆ Overvoltage protection with integrated thermal fuse
- ◆ Suitable for use in industrial and household appliance applications

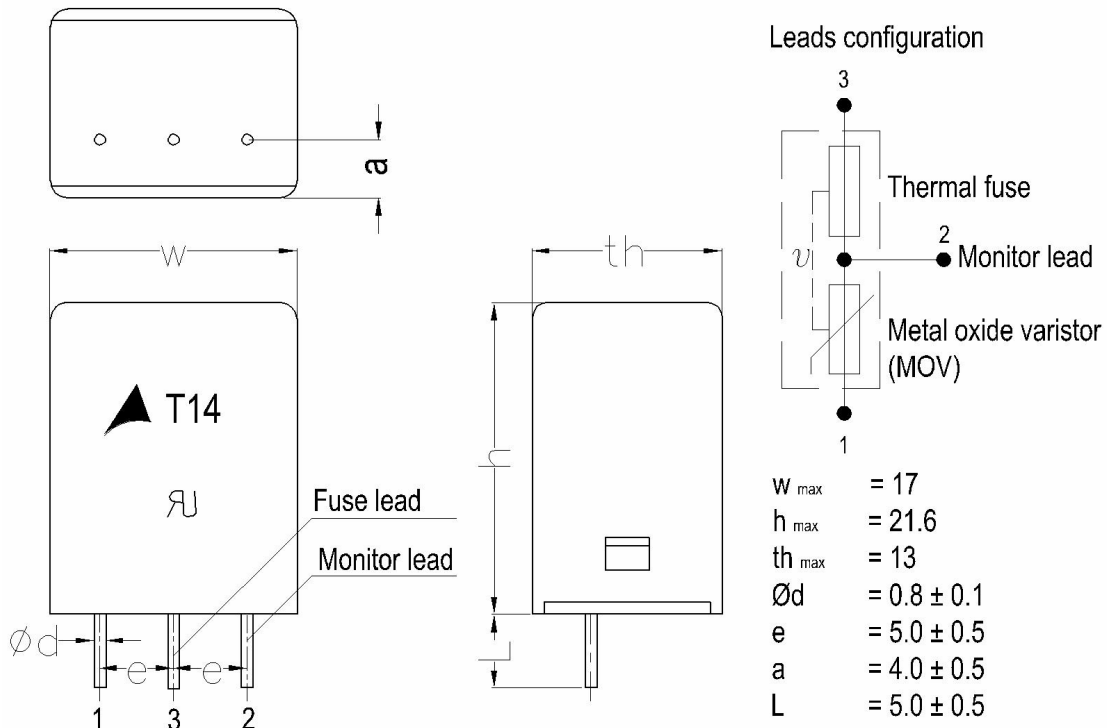
Features

- ◆ Three-leaded version for failure indication
- ◆ UL approval to UL1449 3rd edition (File number E321126)
- ◆ Approval pending to IEC61051 and VDE
- ◆ High peak surge current rating of 6 kA at single 8/20us impulse
- ◆ Meet thermal stability according to section 8.3.5.2 of IEC 61643-11
- ◆ Failure safe for abnormal over voltage (see “Reliability Data Electrical, Abnormal over voltage”)

Nomenclature

- T = EPCOS ThermoFuse varistor
- 14 = Rated disk diameter (mm)
- K = Tolerance of V_V at 1 mA: $\pm 10\%$
- *** = Max. AC voltage (see table on page 3)
- E2 = Energy absorption characteristics, AdvancedD series

Dimensional drawing in mm



General technical data

Climatic category	to IEC 60068-1	40 / 85 / 56
Operating temperature		-40...+85 °C
Storage temperature		-40...+85 °C
Electric strength		≥ 2.5 kV _{RMS}
Insulation resistance		≥ 100 MΩ
Response time		< 25 ns

Electrical data

Maximum ratings (85 °C):

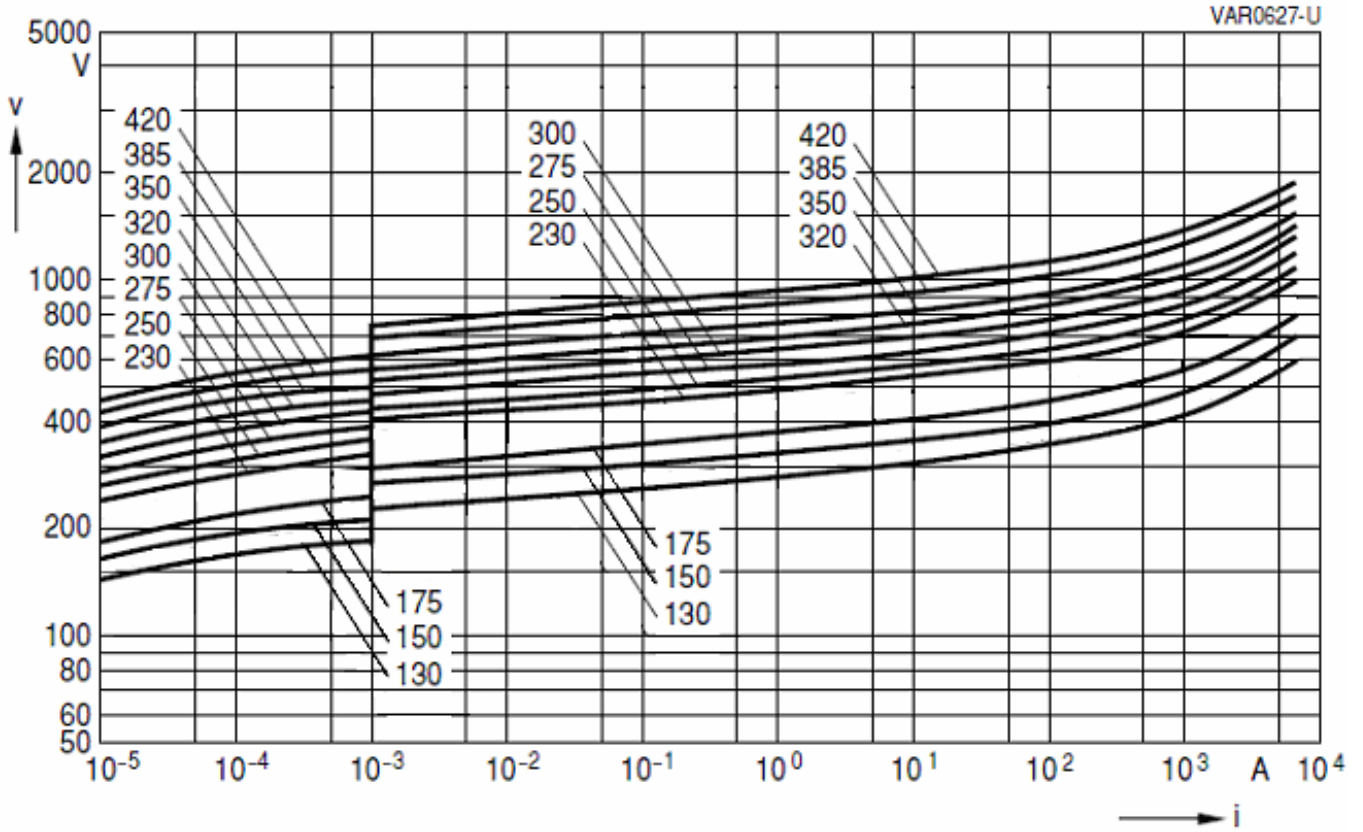
Ordering code	Type	Max. operating AC voltage V _{RMS} [V]	Max. operating DC voltage [V]	Surge current I _{max} (8/20 μs) 1 time [A]	Nominal discharge current I _n ¹⁾ (8/20 μs) 15 times [A]	Energy absorption (2 ms) 1 time [J]	Max. average power dissipation P _{max} [W]
B72214T2131K105	T14K130E2	130	170	6000	3000	50	0.6
B72214T2151K105	T14K150E2	150	200	6000	3000	60	0.6
B72214T2171K105	T14K175E2	175	225	6000	3000	70	0.6
B72214T2231K105	T14K230E2	230	300	6000	3000	90	0.6
B72214T2251K105	T14K250E2	250	320	6000	3000	100	0.6
B72214T2271K105	T14K275E2	275	350	6000	3000	110	0.6
B72214T2301K105	T14K300E2	300	385	6000	3000	125	0.6
B72214T2321K105	T14K320E2	320	420	6000	3000	136	0.6
B72214T2351K105	T14K350E2	350	460	6000	3000	150	0.6
B72214T2381K105	T14K385E2	385	505	6000	3000	165	0.6
B72214T2421K105	T14K420E2	420	560	6000	3000	180	0.6

Characteristics (25 °C):

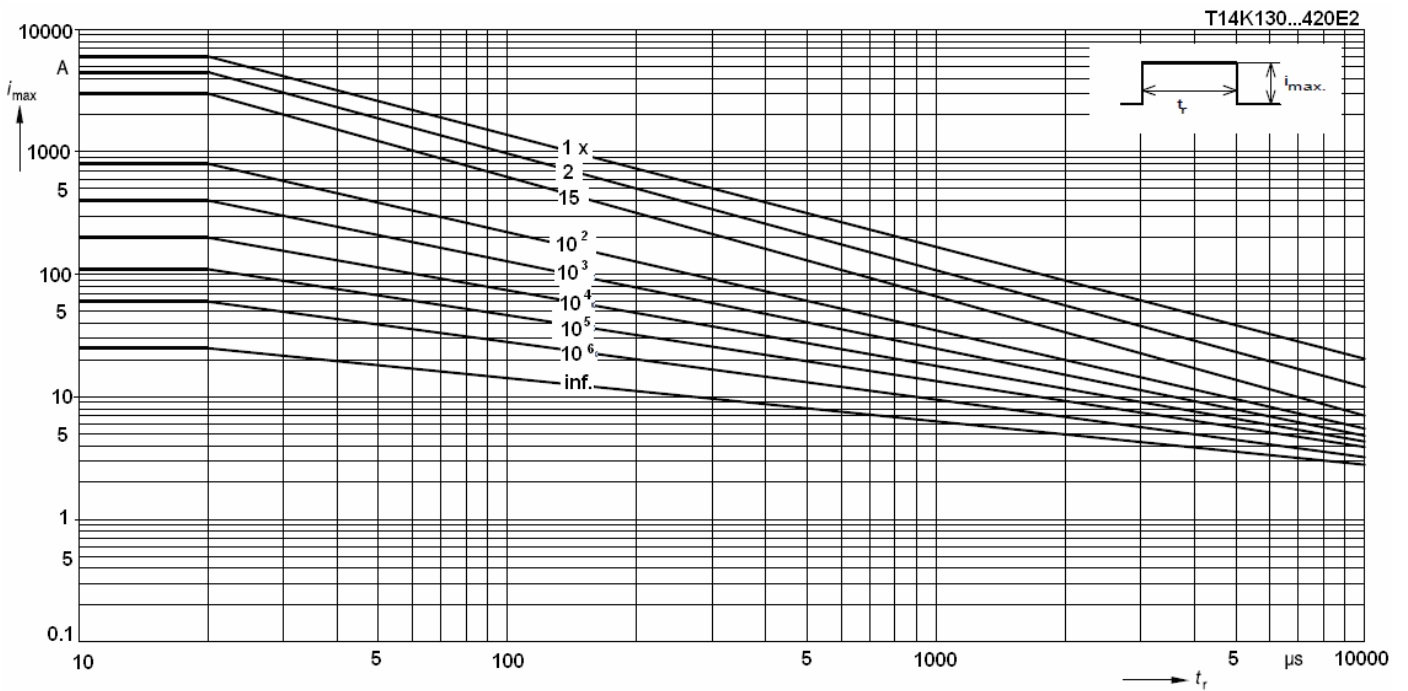
Ordering code	Type	Varistor voltage V _V at 1 mA [V]	Clamping voltage V _C at 50 A (8 / 20 μs) [V]	Typ. capacitance C _{typ} at 1 kHz [pF]
B72214T2131K105	T14K130E2	205 ±10%	340	880
B72214T2151K105	T14K150E2	240 ±10%	395	750
B72214T2171K105	T14K175E2	270 ±10%	455	670
B72214T2231K105	T14K230E2	360 ±10%	595	530
B72214T2251K105	T14K250E2	390 ±10%	650	490
B72214T2271K105	T14K275E2	430 ±10%	710	440
B72214T2301K105	T14K300E2	470 ±10%	775	400
B72214T2321K105	T14K320E2	510 ±10%	840	370
B72214T2351K105	T14K350E2	560 ±10%	910	340
B72214T2381K105	T14K385E2	620 ±10%	1025	315
B72214T2421K105	T14K420E2	680 ±10%	1120	290

1) Note: nominal discharge current is the specification defined in UL1449 3rd and tested with 8/20μs current waveform.

v/i characteristic

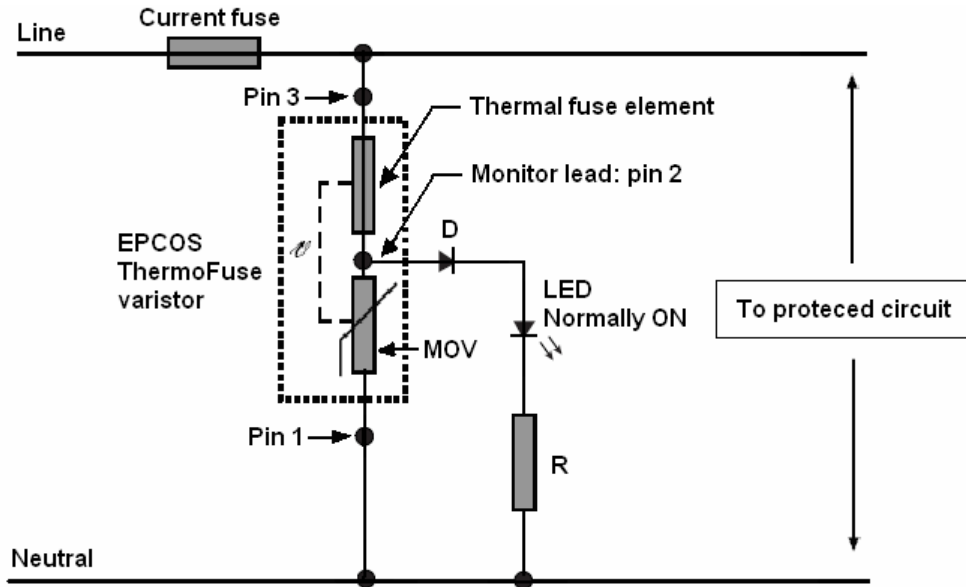


Derating curves



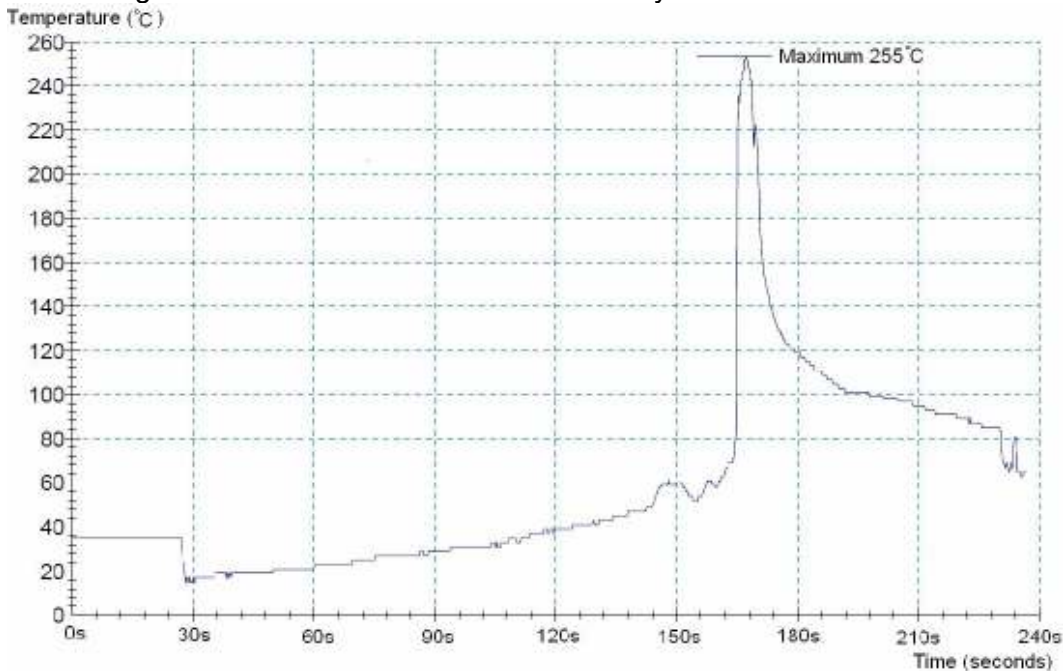
Typical application

The typical application below shows how the monitor lead on the device can be used to indicate that the thermal fuse has been opened. This denotes that the circuit will be no longer protected from surge currents by the MOV after the thermal fuse forms open circuit.

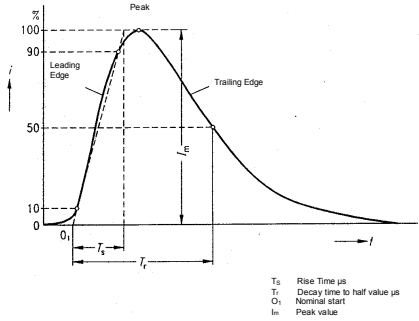


Typical wave soldering curve

Care must be taken when soldering the device into place because it contains a thermal fuse element. Two soldering methods are possible: (1) Manual soldering under max. 350°C / 3s: it is recommended to heat-sink the leads of the device. (2) Wave soldering: it is very important that the temperatures of all preheat stages and the solder bath should be strictly controlled.



Reliability Data Electrical

Characteristics	Test methods / Description	Specifications
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called V_V (1 mA _{DC} @ 0.2 ... 2 s).	To meet the specified value.
Clamping voltage	<p>The maximum voltage between two terminals with the specified standard impulse current (8/20 μs) illustrated below applied.</p> 	To meet the specified value.
Surge current derating, 8/20 μs	10 surge currents (8/20 μs), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 μs	$ \Delta V/V (1 \text{ mA}) \leq 10\%$ (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	10 surge currents (2ms), unipolar, interval 120s, amplitude corresponding to derating curve for 10 impulses at 2 ms	$ \Delta V/V (1 \text{ mA}) \leq 10\%$ (measured in direction of surge current) No visible damage



Characteristics	Test Methods/Description	Specifications																																				
<p>Abnormal over voltage</p>	<p>This device is designed to form open circuit in the event of overheating due to the limited current abnormal over voltage conditions as outlined in section 39.4 of UL1449 3rd edition.</p> <p>The device (pin 1 & 3) is to be connected to a power supply having an open circuit voltage equal to the test voltage specified below. The power supply is to incorporate a series variable resistor that can be adjusted to obtain the short-circuit current (I_{sc}). The variable resistor is to be adjusted such that I_{sc} equals 10A, 5A, 2.5A, 0.5A or 0.125A respectively (measured without the device in the circuit). The device will be energized for 7 hours, or until the device becomes disconnected from the power supply, or until current to, or temperature within the device attains equilibrium ²⁾.</p> <p>The test result will be visually inspected.</p> <p>Detailed test voltages applied onto the devices are as below:</p> <table border="1" data-bbox="408 1182 1077 1736"> <thead> <tr> <th>Type</th> <th>Device rating (V ac)</th> <th>Test voltage (V ac)</th> </tr> </thead> <tbody> <tr><td>T14K130E2</td><td>130</td><td>260</td></tr> <tr><td>T14K150E2</td><td>150</td><td>300</td></tr> <tr><td>T14K175E2</td><td>175</td><td>350</td></tr> <tr><td>T14K230E2</td><td>230</td><td>415</td></tr> <tr><td>T14K250E2</td><td>250</td><td>500</td></tr> <tr><td>T14K275E2</td><td>275</td><td>480</td></tr> <tr><td>T14K300E2</td><td>300</td><td>600</td></tr> <tr><td>T14K320E2</td><td>320</td><td>600</td></tr> <tr><td>T14K350E2</td><td>350</td><td>600</td></tr> <tr><td>T14K385E2</td><td>385</td><td>600</td></tr> <tr><td>T14K420E2</td><td>420</td><td>600</td></tr> </tbody> </table>	Type	Device rating (V ac)	Test voltage (V ac)	T14K130E2	130	260	T14K150E2	150	300	T14K175E2	175	350	T14K230E2	230	415	T14K250E2	250	500	T14K275E2	275	480	T14K300E2	300	600	T14K320E2	320	600	T14K350E2	350	600	T14K385E2	385	600	T14K420E2	420	600	<p>Any of below phenomena shall not be observed, otherwise this device will be judged as failed part:</p> <ol style="list-style-type: none"> 1. Emission of flame, molten metal, glowing or flaming particles through any openings (pre-existed or created as a result of the test) in the device. 2. Charring, glowing, or flaming of the supporting surface, or cheesecloth draped on the device. 3. Ignition of the enclosure. 4. Creation of any openings in the enclosure that result in accessibility of live parts.
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T14K420E2	420	600																																				

2) Note:

Thermal fuse may not form open circuit under low current [e.g. 0.125A] due to less heat generated by MOV, however the device will reach thermal equilibrium within 30 minutes under a low temperature which will not be able to cause any damage to the device.

Cautions and warnings

General

1. EPCOS metal oxide varistors (SIOVs) are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
2. Ensure suitability of SIOVs through reliability testing during the design-in phase. The SIOVs should be evaluated taking into consideration worst-case conditions.
3. For applications of SIOVs in line-to ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

1. Store SIOVs only in original packaging. Do not open the package before storage.
2. Storage conditions in original packaging:

Storage temperature:	-25 °C ... +45 °C
Relative humidity:	<75% annual average, <95% on maximum 30 days a year.
Dew precipitation:	Is to be avoided.
3. Avoid contamination of SIOVs surface during storage, handling and processing.
4. Avoid storage of SIOVs in harmful environments which can affect the function during long-term operation (examples given under operation precautions).
5. The SIOV type series should be soldered within the time specified.

SIOV-S, -Q, -LS	24 month
T, ETFV and SFS types	12 month.

Handling

1. SIOVs must not be dropped.
2. Components must not be touched with bare hands. Gloves are recommended.
3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

1. Use rosin-type flux or non-activated flux.
2. Insufficient preheating may cause ceramic cracks.
3. Rapid cooling by dipping in solvent is not recommended.
4. Complete removal of flux is recommended.

Mounting

1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason the SIOVs should be physically shielded from adjacent components.

Operation

1. Use SIOVs only within the specified temperature operating range
2. Use SIOVs only within the specified voltage and current ranges.
3. Environmental conditions must not harm the SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in the presence of deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas, etc), corrosive agents, humid or salty conditions, Avoid contact with any liquids and solvents.

Important notes

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